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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/727,138	12/03/2003	Kaushik Saha	852463.406	5322
	7590 04/01/200 LECTUAL PROPERTY	EXAM	EXAMINER	
701 FIFTH AVENUE, SUITE 5400			DO, CHAT C	
SEATTLE, WA	A 98104-7092		ART UNIT	PAPER NUMBER
			2193	
			MAIL DATE	DELIVERY MODE
			04/01/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/727,138	SAHA ET AL.		
Examiner	Art Unit		
CHAT C. DO	2193		

	CHAT C. DO	2193						
The MAILING DATE of this communication appe	ars on the cover sheet with the o	orrespondence add	ress					
THE REPLY FILED 11 March 2008 FAILS TO PLACE THIS AP	PLICATION IN CONDITION FOR	ALLOWANCE.						
application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appe	☑ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of thi application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 4.1.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time							
a) The period for reply expiresmonths from the mailing	date of the final rejection.							
no event, however, will the statutory period for reply expire to	The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no work, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TW.							
MONTHS OF THE FINAL REJECTION. See MPEP 706.07(	n.							
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filled is the date for purposes of determining the period of ext under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patient term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corresponding amount of shortened statutory period for reply origing than three months after the mailing date	of the fee. The appropria nally set in the final Office	ate extension fee e action; or (2) as					
The Notice of Appeal was filed on A brief in comp.	liance with 37 CEP 41 37 must be t	Flad within two months	of the date of					
filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed w	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of the						
<u>AMENDMENTS</u>								
The proposed amendment(s) filed after a final rejection, to  (a) They raise new issues that would require further core  (b) They raise the issue of new matter (see NOTE belo  (c) They are not deemed to place the application in bet	nsideration and/or search (see NOT w);	E below);						
(c) in they are not deemed to place the application in bet appeal; and/or	ter form for appeal by materially rec	lucing or simplifying ti	ne issues for					
(d) ☐ They present additional claims without canceling a c	corresponding number of finally reje	ected claims.						
NOTE: (See 37 CFR 1.116 and 41.33(a)).								
4. The amendments are not in compliance with 37 CFR 1.12		mpliant Amendment (I	PTOL-324).					
5. Applicant's reply has overcome the following rejection(s):								
<ol> <li>Newly proposed or amended claim(s) would be all non-allowable claim(s).</li> </ol>	owable if submitted in a separate, t	imely filed amendmer	it canceling the					
7. To purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is provided in the control of the con	7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.							
The status of the claim(s) is (or will be) as follows:								
Claim(s) allowed: Claim(s) objected to:								
Claim(s) rejected: 1-7 and 10-20.								
Claim(s) withdrawn from consideration:								
AFFIDAVIT OR OTHER EVIDENCE								
<ol> <li>The affidavit or other evidence filed after a final action, bu because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>								
<ol> <li>The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessary</li> </ol>	vercome all rejections under appea	l and/or appellant fail:	to provide a					
10. The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER	n of the status of the claims after er	ntry is below or attach	ed.					
The request for reconsideration has been considered bu See Continuation Sheet.	t does NOT place the application in	condition for allowan	ce because:					
12.  Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s)  13.  Other:								
	/Chat C. Do/ Primary Examiner, Art U	nit 2103						
	i illiary Examiner, Art O	1111 2 100						

U.S. Patent and Trademark Office PTOL-303 (Rev. 08-06)

Continuation of 11. does NOT place the application in condition for allowance because: The applicant argued in page 7 fourth paragraph for claims rejected under 35 U.S.C. 101 that the current amended version would overcome the rejection by prossing a digital signal. In addition claim 3 is not a software claim since recites means for storing the inputs and outputs of the system to process a digital signal. The examiner respectfully subsmit that the current amendment does not overcome the 101 rejection as citied in the previous Office action since the claims just merely disclose series steps for performing FFI/IFFFT without clearly disclosing a practical application. Further the claims appear to premeting every substantial practical application of the idea emobided by the claims. Claim 3 is considered as a software per se since every means is software module for performing the intended function. Nothing in the claim would indicate or utilize hardware to perform the intended function rather software modules.

The applicant argued in page 7 for claims 5-6 that there is no explaination of why claims 5 and 16 are directed to non-statutory subject matter.

The examiner respectfully submits that the previous Office action clearly address how and why these claims are directed to non-statutory subject matter. IN ADDITION to the above rejection under 35 U.S.C. 101, claims 5 and 16 merely disclose a computer readable medium without clearly address that the medium is tangible and executed by a computer. Without executing by a computer, the computer-readable medium is just a storage which cannot perform the intended functions.

The applicant argued in page 8 for claims rejected under 35 U.S.C. 103(a) that the cited references by Abel and Jaber fail to disclose the "linear scalable" method wherein the specification defines the "linear scalabling" as the computation time retoning in inverse proportion to the number of processors in the multiprocessor solution and the step of "distributing...in the stage" would support the linear scalable method as claimed.

The examiner respectfully submits that the definition of finder scalability as "the computation time reducing in inverse proportion to the number of processors in the multiprocessor solution" is addressed into a processor in the multiprocessor solution. In addition, the step "distribution to stage" has no direct correlated to the definition of the "linear scalability" as "the computation time reducing in inverse proportion to. In the number of processors in the multiprocessor solution". Thus, the "linear scalabile" is not given any patentable weight because it is reclied in the preamble of the claim. In general, the combination of references by Abel and Jaber clearly discloses reasonably every since limitations cited in the claims either individually or in combination.

The applicant argued in pages 9-10 for claims 1, 3, and 5 that neither Abel nor Jaber teach, suggest or motivate a linear scalable method comprising a first plurality of stages employing a plurality of butterfly operations having a first radix, where not of the butterfly operations in each stage in the first plurality of stages has a single, un-nested computation loop of the first radix as cited in the claimed invention. The examiner respectfully submits that this particular features are very common/standard in FFT as clearly seen in primary reference by Abel et al. 5 Figures 1-14. Abel et al. disclose in Figures 1-14 a linear scalable method for computing a Fast Fourier Transform (FFT) or Inverse Fast Fourier transform (FFT) in a system (e.g., abstract, Figures 7 and 11 wherein Figure 7 discloses an IFFT and Figure 11 discloses a FFT justing a decimation in time approach (e.g., laist line of abstract and col. 13 line 65 to col. 14 line 12), comprising the steps of: computing an N-point FFT/IFT of a signal (e.g. either seen in Figures 7-8 or Figure 11 for IFFT/FFT respectively) using a first plurality of butterfly operations on components 800 and 805, each stage in the first plurality of stages employing a plurality of butterfly operations in each stage. 2 set first radix size) wherein each of the butterfly operations in each stage. Components 800, 805, and 810 in Figure 8) in the first plurality of stages has a single, un-nested computation loop of the first radix (e.g. Figure 4 and Figure 8 wherein the first plurality of stages has a single, un-nested computation loop of the first radix (e.g. Figure 4 and Figure 8 wherein there is no loopback/fivedback for computing the IFFT/FFT).